Amendments to the Claims

1. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ comprising a network and a plurality of modules ~~(M_1, M_2, M_3 up to and including M_n)~~, which are arranged to communicate to each other via the network, wherein the network is arranged to establish transactions between a first module ~~(M_1)~~ and at least two second modules ~~(M_2, M_3 up to and including M_n)~~, the network being arranged to send a plurality of requests ~~(REQ)~~ from the first module to the second modules, and wherein the second modules are arranged to generate individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ indicating a result of the execution of the requests ~~(REQ)~~, characterized in that the network is arranged to generate a single response ~~(SRESP)~~ to the first module ~~(M_1)~~, dependent on the individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ of the second modules ~~(M_2, M_3 up to and including M_n)~~.

2. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 1, wherein the network comprises a network interface (NI) to generate the single response ~~(SRESP)~~ to the first module ~~(M_1)~~.

3. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 1, wherein the single response ~~(SRESP)~~ has a value which is dependent on a specific function of the individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ of the second modules.

4. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 3, wherein the specific function is defined such that the value of the single response ~~(SRESP)~~ indicates that at least one of the second modules ~~(M_2, M_3 up to and including M_n)~~ has successfully executed the requests ~~(REQ)~~ issued by the first module ~~(M_1)~~.

5. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 3, wherein the specific function is defined such that the value of the single response ~~(SRESP)~~ indicates that each of the second modules ~~(M_2, M_3 up to and including M_n)~~ has successfully executed the requests ~~(REQ)~~ issued by the first module ~~(M_1)~~.

6. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 3, wherein the specific function is defined such that the value of the single response ~~(SRESP)~~ indicates a success if no error occurred and the value of the single response indicates a failure if at least one error occurred, wherein the value of the single response represents the most serious error.

7. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 3, wherein the specific function is
defined such that the value of the single response ~~(SRESP)~~ indicates which types of error have occurred during execution of the requests ~~(REQ)~~.

8. (CURRENTLY AMENDED) Integrated circuit ~~(IC)~~ according to claim 1, wherein the individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ carry data parts transmitted by the second modules ~~(M_2, M_3 up to and including M_n)~~, the single response ~~(SRESP)~~ comprising the data parts and indicating which data parts originate from which second modules.

9. (CURRENTLY AMENDED) Method for establishing transactions in an integrated circuit (IC) comprising a network and a plurality of modules ~~(M_1, M_2, M_3 up to and including M_n)~~, the transactions between the modules being established via the network, wherein the network sends a plurality of requests ~~(REQ)~~ from a first module ~~(M_1)~~ to at least two second modules ~~(M_2, M_3 up to and including M_n)~~, and wherein the second modules generate individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ indicating a result of the execution of the requests ~~(REQ)~~, characterized in that the network generates a single response (SRESP) to the first module ~~(M_1)~~, dependent on the individual responses ~~(RESP2, RESP3 up to and including RESPn)~~ of the second modules ~~(M_2, M_3 up to and including M_n)~~.